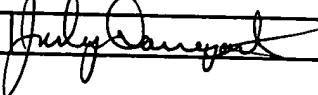




IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231, on the below date of deposit.

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Inventor(s): Suryanarayana Duggirala, Rohit Kapur, Thomas W. Williams

Serial No.: 09/275,502

Group Art Unit: 2825

Filed: 03/24/1999

Examiner: Siek, Vuthe

Confirmation No: 9850

Title: SYSTEM AND METHOD FOR HIGH-LEVEL TEST PLANNING FOR LAYOUT

Assistant Commissioner for Patents
Washington, D.C. 20231



Sir:

TRANSMITTAL OF FORMAL DRAWINGS

In response to Drawing Informalities

attached please find:

(a) the formal drawings for this application
Number of Sheets 8

Each sheet of drawing indicates the identifying indicia suggested in § 1.84(c) on the reverse side of the drawing

(b) a copy of the NOTICE OF INFORMAL DRAWINGS

Please direct all correspondence concerning the above-identified application to the following address:

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(408) 938-9060

Respectfully submitted,

Date: June 7, 2002

By: 
Anthony C. Murabito
Reg. No. 35,295